

ABSTRACT OF THE DISCLOSURE

PROCESSING PROCEDURE FOR AN ELECTRONIC SYSTEM SUBJECT  
TO TRANSIENT ERROR CONSTRAINTS AND A MEMORY ACCESS  
MONITORING DEVICE

5 This invention relates to a processing procedure  
for an electronic system subject to transient error  
constraints, in which two virtual sequences installed  
on a single physical sequence are multiplexed in time  
10 in one given real time cycle (the data resulting from  
each execution of a virtual sequence being stored so  
that they can be voted before use), and in which if an  
error is detected, the real time cycle in progress is  
inhibited and a healthy context is reloaded to make a  
10 restart that consists of a nominal execution of the  
next cycle starting from the reloaded context.

This invention also relates to a memory access  
monitoring device.

15 Figure 5B.